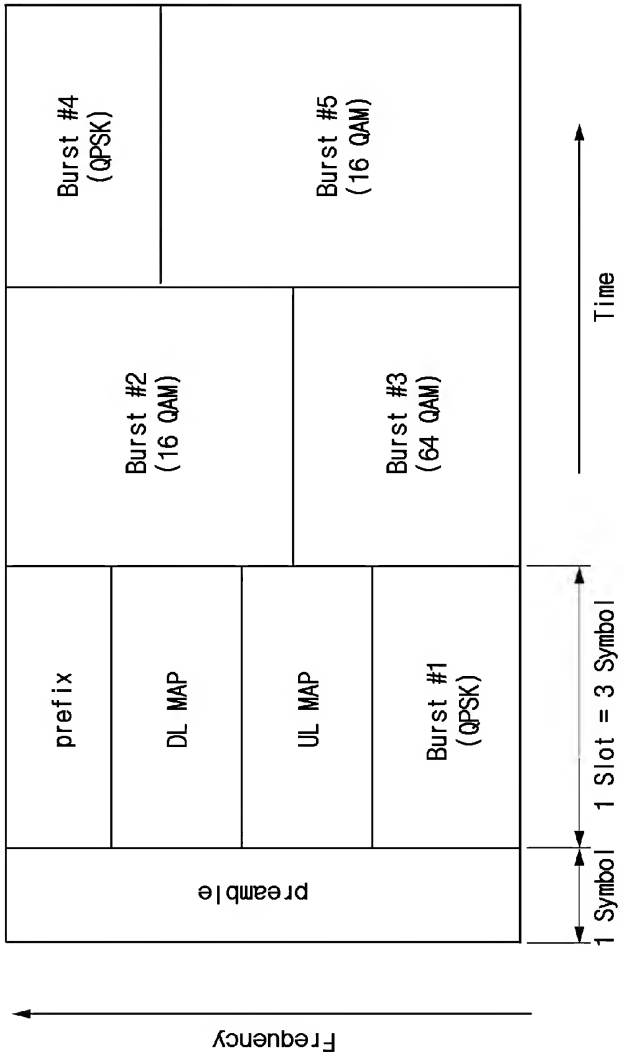
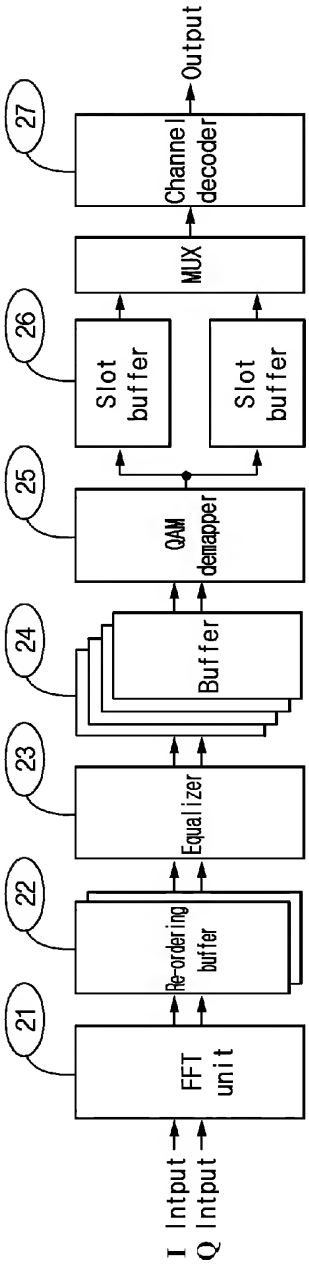
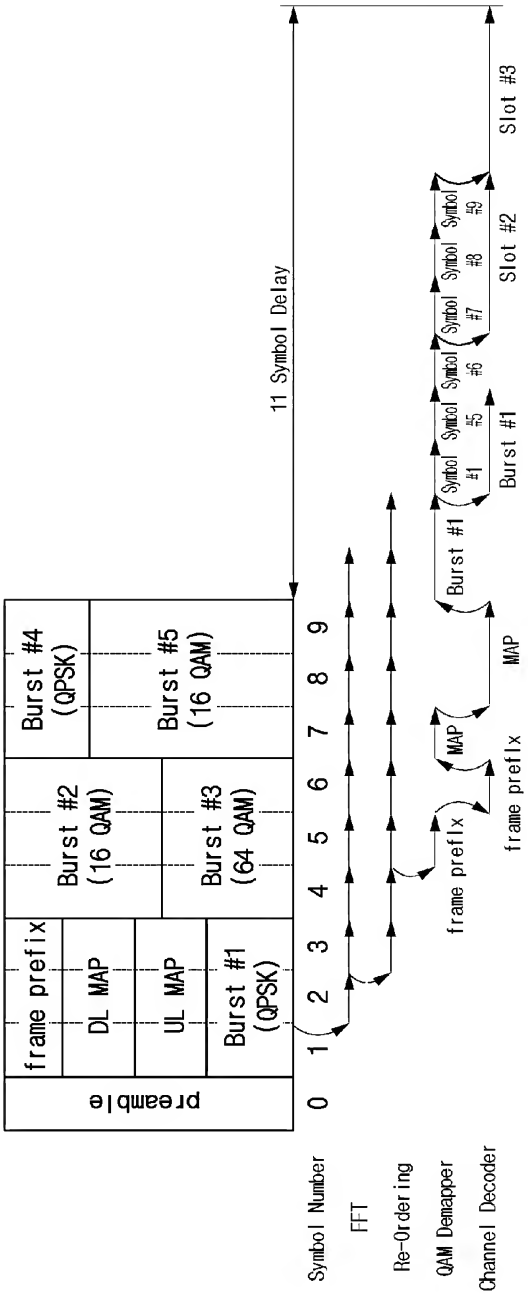
[Fig. 1]


Burst #4 (QPSK)
Burst #5 (16 QAM)
Burst #2 (16 QAM)
Burst #3 (64 QAM)
prefix
DL MAP
UL MAP
Burst #1 (QPSK)
preamble
Time
1 Slot = 3 Symbol
1 Symbol
Frequency

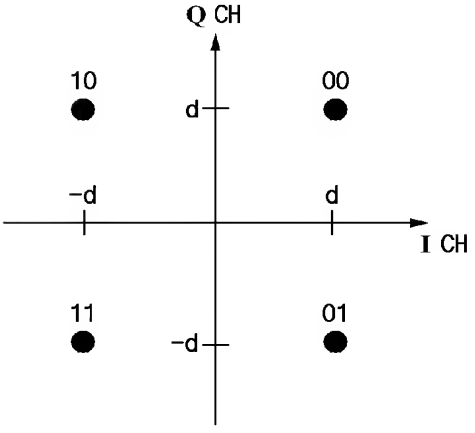
[Fig. 2]


I Intput
Q Intput
FFT unit
Re-ordering buffer
Equalizer
Buffer
QAM demapper
Slot buffer
Slot buffer
MUX
Channel decoder
Output
21
22
23
24
25
26
27

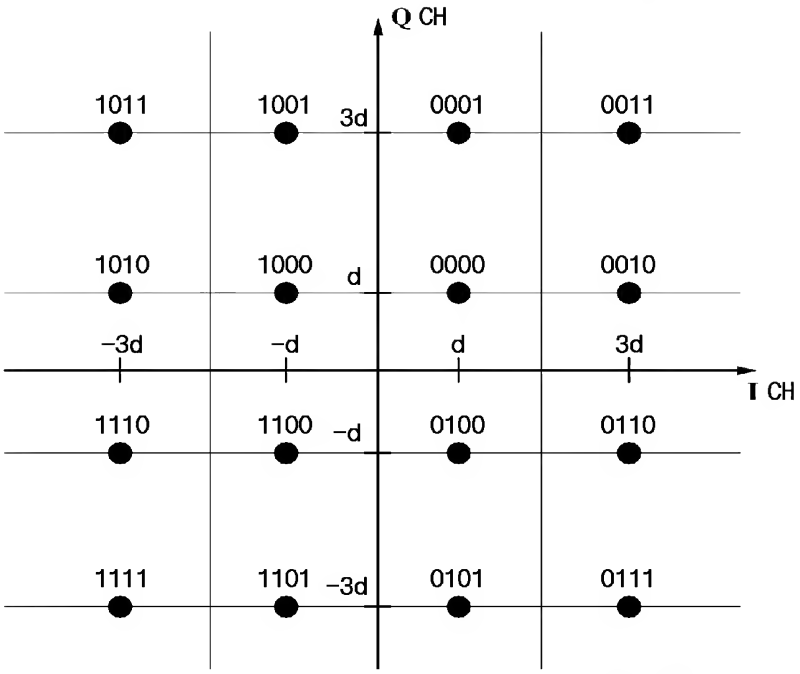
[Fig. 3]


preamble
frame prefix
DL MAP
UL MAP
Burst #1 (QPSK)
Burst #2 (16 QAM)
Burst #3 (64 QAM)
Burst #4 (QPSK)
Burst #5 (16 QAM)
0 1 2 3 4 5 6 7 8 9
11 Symbol Delay
Slot #3
Slot #2
Burst #1
Burst #1
Symbol #1
Symbol #5
Symbol #6
Symbol #7
Symbol #8
Symbol #9
frame prefix
MAP
frame prefix
MAP
Symbol Number
FFT
Re-Ordering
QAM Demapper
Channel Decoder

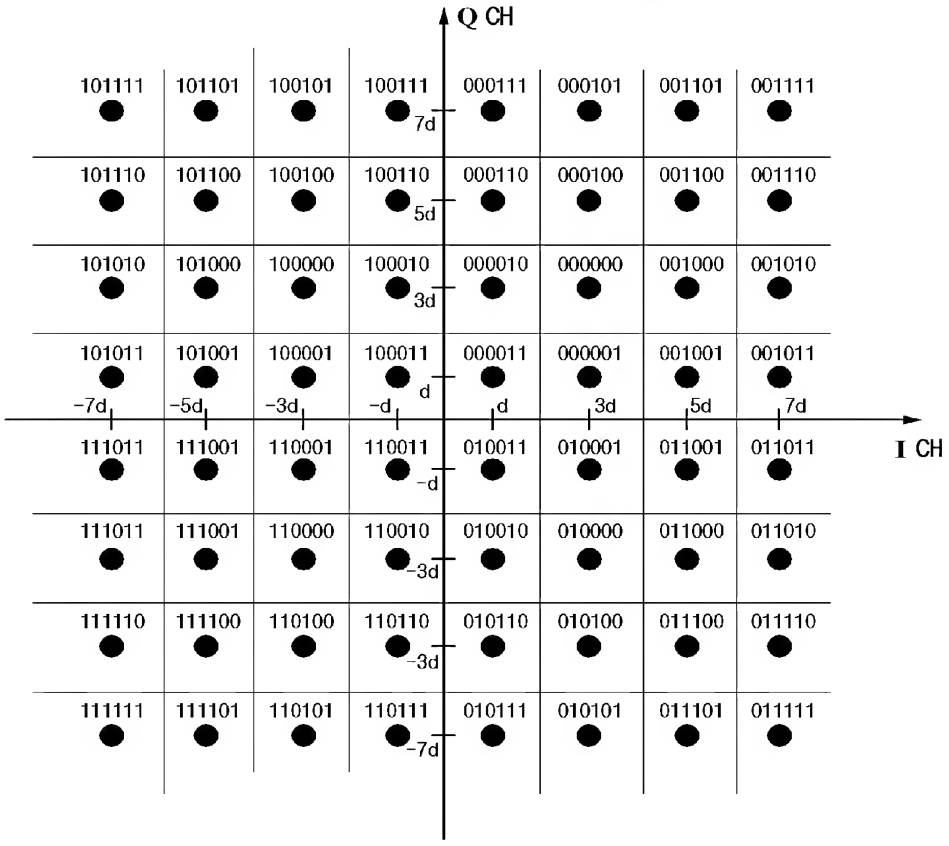
[Fig. 4]


Q CH
I CH
10
00
11
01
d
-d
d
-d

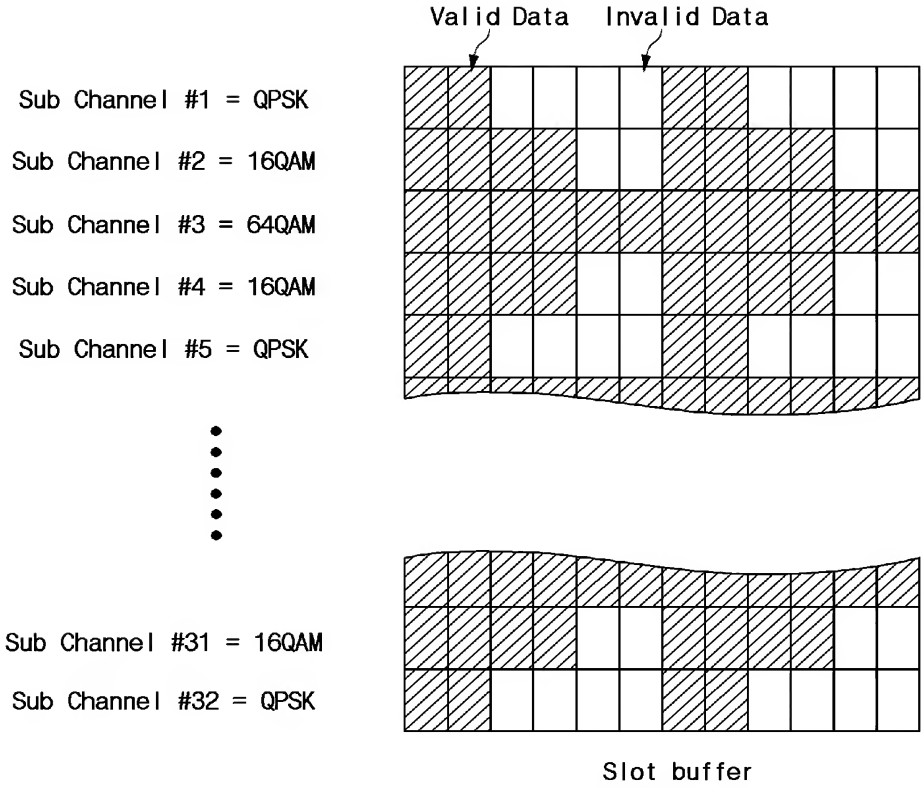
[Fig. 5]


Q CH
1011
1001
3d
0001
0011
1010
1000
d
0000
0010
-3d
-d
d
3d
I CH
1110
1100
-d
0100
0110
1111
1101
-3d
0101
0111

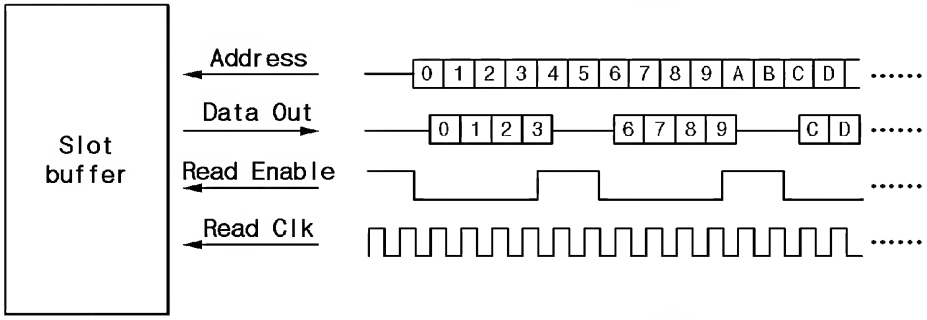
[Fig. 6]


Q CH
101111
101101
100101
100111
7d
000111
000101
001101
001111
101110
101100
100100
100110
5d
000110
000100
001100
001110
101010
101000
100000
100010
3d
000010
000000
001000
001010
101011
101001
100001
100011
d
000011
000001
001001
001011
-7d
-5d
-3d
-d
d
3d
5d
7d
111011
111001
110001
110011
-d
010011
010001
011001
011011
I CH
111011
111001
110000
110010
-3d
010010
010000
011000
011010
111110
111100
110100
110110
-3d
010110
010100
011100
011110
111111
111101
110101
110111
-7d
010111
010101
011101
011111

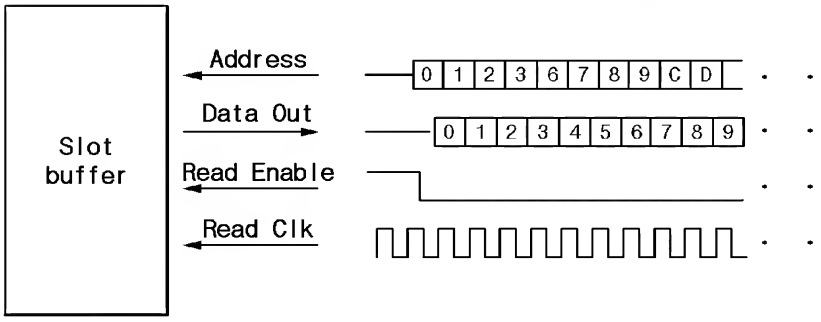
[Fig. 7]



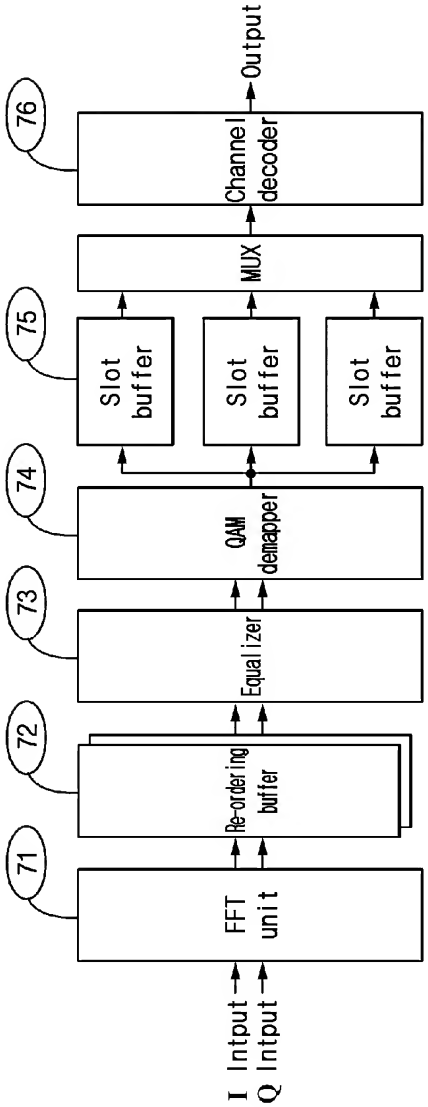
[Fig. 8]



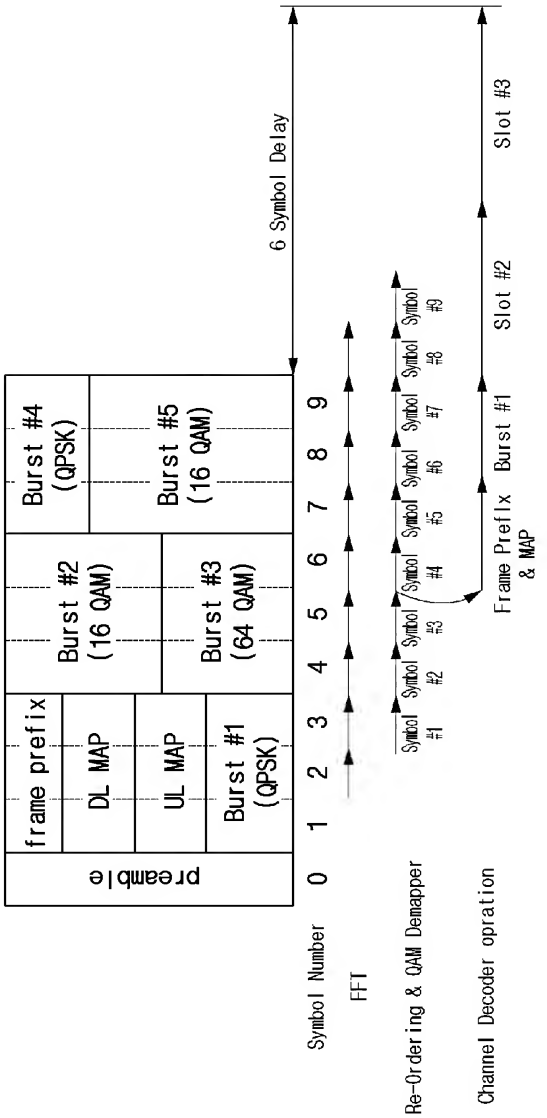
[Fig. 9]



[Fig. 10]


I Intput
Q Intput
71
FFT unit
72
Re-ordering buffer
73
Equalizer
74
QAM demapper
75
Slot buffer
Slot buffer
Slot buffer
MUX
76
Channel decoder
Output

[Fig. 11]


preamble
frame prefix
DL MAP
UL MAP
Burst #1 (QPSK)
Burst #2 (16 QAM)
Burst #3 (64 QAM)
Burst #4 (QPSK)
Burst #5 (16 QAM)
0 1 2 3 4 5 6 7 8 9
6 Symbol Delay
Symbol Number
FFT
Re-Ordering & QAM Demapper
Channel Decoder opration
Symbol #1
Symbol #2
Symbol #3
Symbol #4
Symbol #5
Symbol #6
Symbol #7
Symbol #8
Symbol #9
Frame Prefix & MAP
Burst #1
Slot #2
Slot #3